



迈拓电子
MAITUO ELECTRONIC

DTA114EM Series Digital Transistor (Built-In Resistors)

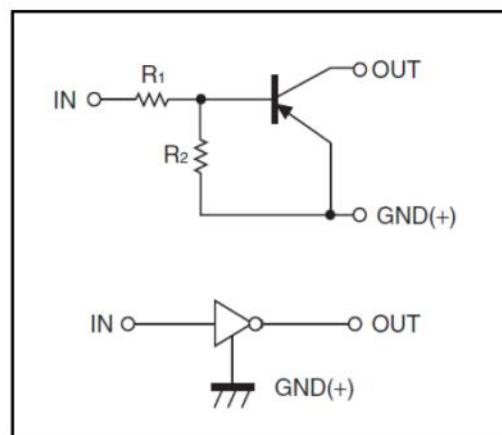
Digital transistor (PNP)

FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easier
- AEC-Q101 qualified

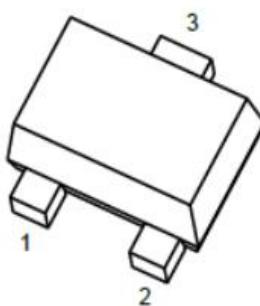
Marking : 14

PIN CONNECTIONS AND MARKING



DTA114EM

SOT-723



1. IN
2. GND
3. OUT



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MAXIMUM RATINGS ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value						Unit
		M	E	UA	CA	KA	SA	
Supply voltage	V _{CC}	-50						V
Input voltage	V _{IN}	-40 ~ 10						V
Output current	I _O ¹⁾	-50						mA
Peak collector current	I _{CM} ¹⁾	-100						mA
Maximum power dissipation	P _D ¹⁾	100	150	200	200	200	300	mW
Operating junction and storage temperature range	T _j , T _{stg}	-55 ~ 150						°C

ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$ unless otherwise specified)

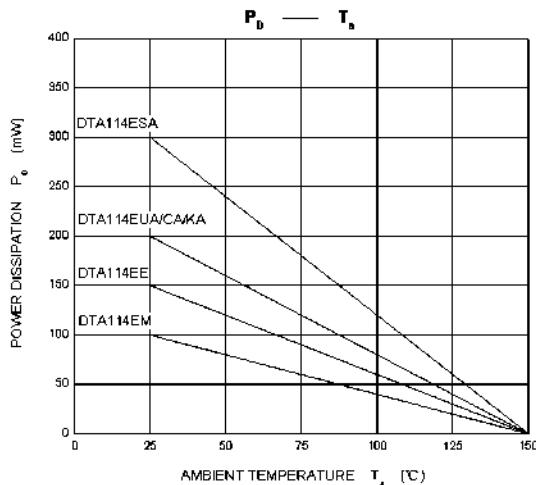
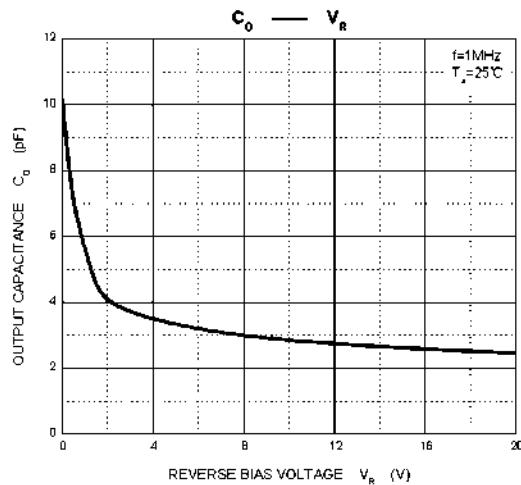
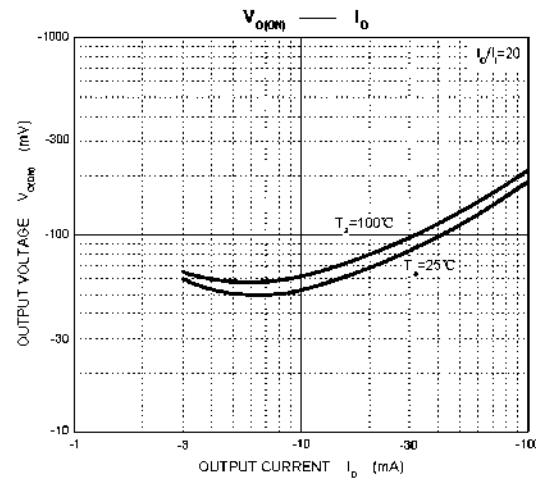
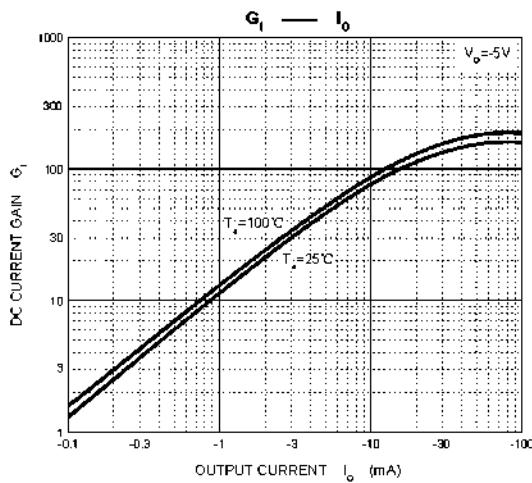
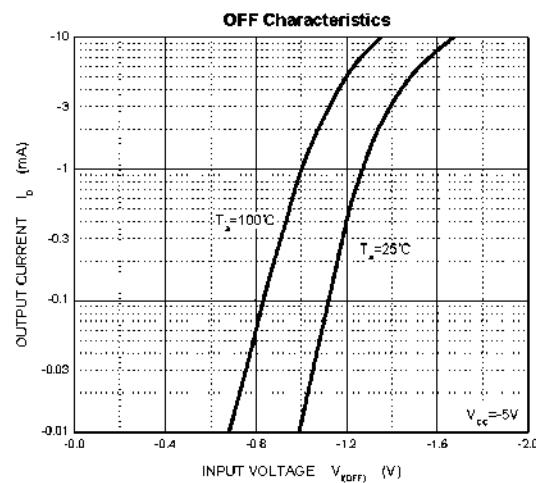
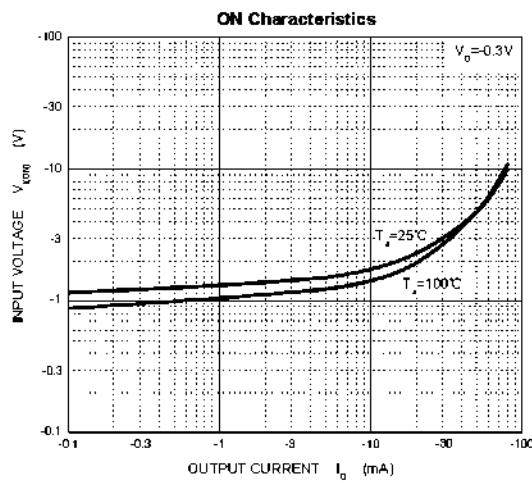
Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} = -5V, I _O = -100μA	-0.5	-	-	V
	V _{I(on)}	V _O = -0.3V, I _O = -10mA	-	-	-3	
Output voltage	V _{O(on)}	I _O /I _I = -10mA/-0.5mA	-	-	-0.3	V
Input current	I _I	V _I = -5V	-	-	-0.88	mA
Output current	I _{O(off)}	V _{CC} = -50V, V _I = 0V	-	-	-0.5	μA
DC current gain	G _I	V _O = -5V, I _O = -5mA	30	-	-	-
Input resistance	R _I		7	10	13	kΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O = -10V, I _O = -5mA, f = 100MHz	-	250	-	MHz

1) Maximum allowed temperature T_j = 25°C.



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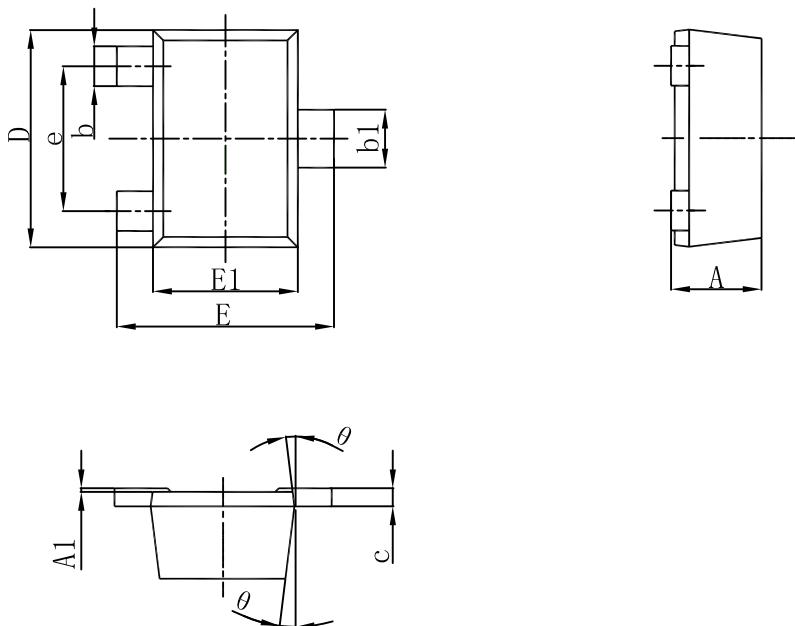
TYPICAL CHARACTERISTICS





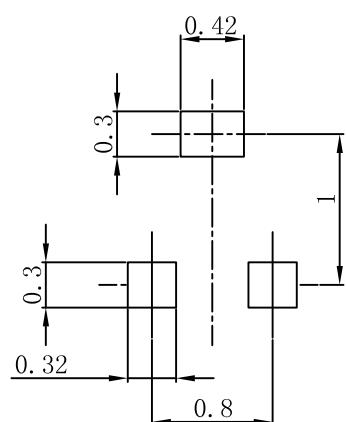
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SOT-723 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.450	0.550	0.018	0.022
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
c	0.080	0.150	0.003	0.006
D	1.150	1.250	0.045	0.049
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
e	0.800TYP.		0.031TYP.	
θ	7° REF.		7° REF.	

SOT-723 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.